Notice of References Cited Application/Control No. 10/073,948 Examiner KEITH VICARY Applicant(s)/Patent Under Reexamination RHOADES ET AL. Page 1 of 1

U.S. PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
*	Α	US-6,907,001	06-2005	Nakayama et al.	370/230
*	В	US-2003/0231627	12-2003	John et al.	370/389
*	С	US-2002/0003795	01-2002	OSKOUY et al.	370/389
*	D	US-7,317,730	01-2008	Devanagondi et al.	370/412
*	Е	US-2002/0075882	06-2002	Donis et al.	370/412
*	F	US-2005/0243829	11-2005	Spencer, Anthony	370/394
*	G	US-2003/0074388	04-2003	Pham et al.	709/106
*	Η	US-7,016,367	03-2006	Dyckerhoff et al.	370/429
*	I	US-6,393,026	05-2002	Irwin, George F.	370/401
*	J	US-6,831,923	12-2004	Laor et al.	370/412
*	К	US-2010/0165991	07-2010	Veal et al.	370/392
*	L	US-2004/0017807	01-2004	Dorr et al.	370/389
	М	US-			

FOREIGN PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Country	Name	Classification
	N					
	0					
	Р					
	Ø					
	R					
	Ø					
	Т					

NON-PATENT DOCUMENTS

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)			
	U	Rai et al. (Packet Processing on a SIMD Stream Processor); Network Processor Design, Volume 3: Issues and Practices, Volume 3 (The Morgan Kaufmann Series in Computer Architecture and Design); Morgan Kaufmann (February 15, 2005); 26 pages			
	V	Seshadri et al. (A Case for Vector Network Processors); Proceedings of the Network Processors Conference West, October 2002, 19 pages			
	w				
	х				

*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).) Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.